



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

BIBDATASHEET

CONFIRMATION NO. 6797

Bib Data Sheet

SERIAL NUMBER 10/681,577	FILING OR 371(c) DATE 10/07/2003 RULE	CLASS 327	GROUP ART UNIT 2816	ATTORNEY DOCKET NO. IMPJ-0004 033327-000036		
APPLICANTS Christopher J. Diorio, Shoreline, WA; Todd E. Humes, Shoreline, WA; Michael Thomas, Seattle, WA;						
** CONTINUING DATA ***** This appln claims benefit of 60/417,072 10/08/2002						
** FOREIGN APPLICATIONS *****						
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 12/31/2003						
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance		STATE OR COUNTRY WA	SHEETS DRAWING 29	TOTAL CLAIMS 51	INDEPENDENT CLAIMS 14	
Verified and Acknowledged _____ Examiner's Signature Initials						
ADDRESS David-B. Ritchie Thelen Reid & Priest LLP P.O. Box 640640 San Jose ,CA 95164-0640						
TITLE Use of analog-valued floating-gate transistors to match the electrical characteristics of interleaved and pipelined circuits						
FILING FEE RECEIVED 1202	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:			<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		